

X-998 US  
10/082,517

PATENT  
Conf. No. 5981



IN THE UNITED STATES PATENT OFFICE

Applicants: Reno L. Sanchez et al.  
Assignee: Xilinx, Inc.  
Title: Method and System for Inserting Probe Points in  
FPGA-Based System-On-Chip

Serial No.: 10/082,517 File Date: 02-22-02  
Examiner: Magid Y. Dimyan Art Unit: 2825  
Docket No.: X-998 US Conf. No.: 5981

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COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

Dear Sir:

In response to the First Office Action mailed from the Patent Office on September 8, 2003, please replace/substitute the following claims as indicated.

01/13/2004 MBELETE1.00000107 240040 10082517  
01 FC:1251 110.00 DA

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